

MAR 16 2007

Application No. 10/039,054  
Reply to Final Office Action of November 16, 2006  
Attorney Docket: 42P12867REMARKS

Claims 1-18 are pending in the application. Claims 1, 7 and 13 are the independent claims. Claims 1, 7 and 13 are sought to be amended. Entry and consideration of this Amendment is respectfully requested. No new matter is believed to have been introduced by this Amendment.

Applicants have made the above Amendment to more particularly point out and distinctly claim the subject matter which Applicants regard as the invention. Based on the above Amendment and the following remarks, Applicants respectfully request that the Examiner reconsider and withdraw all outstanding rejections.

Claim Discussion – 35 USC §102

The Examiner rejected claims 1 and 13 under 35 USC §102(b) as being anticipated by Hobson, US Patent No. 6,122,748. Applicants respectfully traverse this rejection with regard to claims 1 and 13 since Hobson does not teach or suggest amended claims 1 and 13 for at least the following reason.

Independent claims 1 and 13 have been amended to include a similar feature of generating a system management interrupt (SMI) request under ACPI control when a complex task is encountered. Applicants assert that Hobson does not teach or suggest this feature. In fact, Applicants assert that Hobson teaches away from the present claimed invention since Hobson teaches that the BIOS is detecting whether a sleep enable bit is set. Therefore, for at least this reason, claims 1 and 13 are patentable over Hobson.

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Accordingly, Applicants respectfully request that the rejections under 35 U.S.C. § 102(b) be reconsidered and withdrawn.

### Claim Discussion – 35 USC §103

The Examiner rejected claims 2, 3, 7-9, 14 and 15 under 35 USC §103(a) as being unpatentable over Hobson in view of Intel [Intel's SL Enhanced Intel486 Microprocessor Family, June 1993]. Applicants respectfully traverse this rejection with regard to claims 2, 3, 7-9, 14 and 15 since Hobson and Intel, either taken alone or in combination, do not teach or suggest claim 1 (and thus its dependent claims 2 and 3), claim 7 (and thus its dependent claims 8 and 9) and claim 13 (and thus its dependent claims 14 and 15) for at least the following reason.

Independent claims 1, 7 and 13 have been amended to include a similar feature of generating a system management interrupt (SMI) request under ACPI control when a complex task is encountered. Hobson and Intel, either taken alone or in combination, do not teach or suggest this feature. Accordingly, Applicants respectfully request that the rejections under 35 U.S.C. § 103(a) be reconsidered and withdrawn.

### Claims 4-6, 10-12 and 16-18

Applicants are not clear as to the status of claims 4-6, 10-12 and 16-18. The Examiner states in the Office Action Summary that these claims are rejected. But, claims 4-6, 10-12 and 16-18 are not addressed in the Detailed Action. Applicants assert that claims 4-6, 10-12 and 16-18 are patentable over the art of record for at least the reason

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discussed above. Applicants respectfully request clarification of the status of these claims.

**INVITATION FOR A TELEPHONE INTERVIEW**

The Examiner is invited to call the undersigned, Molly A. McCall, at (703) 633-0931 if there remains any issue with allowance of the case.

**CONCLUSION**

Applicants respectfully submit that all of the stated grounds of rejection have been properly traversed accommodated or rendered moot. Applicants believe that a full and complete response has been made to the outstanding Office Action. Thus, Applicants believe that the present application is in condition for allowance, and as such, Applicants respectfully request reconsideration and withdrawal of the outstanding rejections, and allowance of this application.

Respectfully submitted,

Intel Corporation

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